

**Amendments to the Specification:**

Please replace paragraph [0030] with the following amended paragraph:

[0030] Meanwhile, although the potentials of the third node Q23, the fourth node Q24 and the ninth node Q29 are controlled by the power bar signal (powerb) in the present invention by way of an example, the NMOS transistor N31 and N32 may be constructed to control the potential of the first and sixth nodes Q21 and Q26 at the front of the first and second level shifters 22 and 24.